

LIST OF PUBLICATIONS CITED BY APPLICANT			Atty. Docket No. 0553-0168.02	Serial No. Not Assigned		
			Applicant Ritsuko KAWASAKI et al			
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	JP 10-092576	04/10/98	Cambridge Display Technology Ltd.	X		04/18/97

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- 1) HATANO, M. et al, "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT with High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.
- 2) SHIMOKAWA, R. et al, "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, vol. 27, no. 5, pp. 751-758, May, 1988.
- 3) YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, 1997.
- 4) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, 1998.
- 5) SCHENK, H. et al, "Polymers for Light Emitting Diodes," EURODISPLAY '99, Proceedings of the 19th International Display Research Conference, Berlin, Germany, September 6-9, 1999, pp. 33-37.

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